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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 10/448,351 05/30/2003 PAT 6,727,132
 which is a DIV of 10/272,369 10/17/2002 PAT 6,645,799
 which is a DIV of 09/639,306 08/16/2000 PAT 6,503,788

yes ims

** FOREIGN APPLICATIONS *****

JAPAN 2000-17875 01/24/2000

yes ims

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/16/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	JAPAN	7	10	2
Examiner's Signature <i>[Signature]</i>				
Initials <i>[Initials]</i>				

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TITLE

Method of manufacturing a dual gate semiconductor device with a poly-metal electrode

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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